

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Attorney Docket No.: 046124-5287	Application No.: 10/821,272	
				Applicant(s): Seiichiro MIZUNO et al.	PAGE 1 OF 1	
				Filing Date: April 9, 2004	Group: 2821 Unassigned	
U.S. PATENT DOCUMENTS						
Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date
<i>TV</i>	5,898,334	April 27, 1999	Fairgrieve	37	411	May 12, 1997
FOREIGN PATENT DOCUMENTS						
	Document Number	Date	Country	Class	Sub Class	Translation Yes No
<i>TV</i>	JP 63-251945	October 19, 1988	Japan	37	411	Abstract
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
<i>TV</i>	Nagahori et al.; "An Analog Front-End Chip Set Employing an Electro-Optical Mixed Design on SPICE for 5-Gb/s/ch Parallel Optical Interconnection"; IEEE Journal of Solid-State Circuits, Vol. 36, No. 12, December 2001; pp. 1984-1991.					
Examiner	<i>TUYET VO</i>			Date Considered		<i>8/15/05</i>
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PTO Form 1449	Applicants Seiichiro MIZUNO, et al.	Filing Date April 9, 2004
		Group Unsigned <i>2821</i>

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
	Document Number	Date	Country	Class	Sub Class	Translation YES NO
✓	9-83442 ①	March 28, 1997	Japan			Abstract
	6-45675	February 18, 1994	Japan			Abstract
	2001-36187	February 9, 2001	Japan			Abstract
	11-40855 ②	February 12, 1999	Japan			Abstract
	63-67791 ③	March 26, 1988	Japan			Abstract
	58-182144	October 25, 1983	Japan			Abstract
	2000-252756 ④	September 14, 2000	Japan			Abstract
	0909973 ⑤	April 21, 1999	European			NO
✓	2002-64433 ⑥	February 28, 2002	Japan			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner	TYET VO	Date Considered	8/5/05
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